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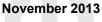
FDB44N25 N-Channel UniFETTM MOSFET 250 V, 44 A, 69 mΩ

Features

- $R_{DS(on)}$ = 69 m Ω (Max.) @ V_{GS} = 10 V, I_D = 22 A
- Low Gate Charge (Typ. 47 nC)
- Low C_{rss} (Typ. 60 pF)
- 100% Avalanche Tested

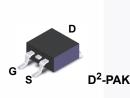
Applications

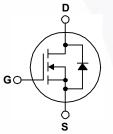
- PDP TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply



Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		FDB44N25	Unit	
V _{DSS}	Drain-Source Voltage	250	V A A	
ID	Drain Current	44 26.4		
I _{DM}	Drain Current	- Pulsed (Note 1)	176	А
V _{GSS}	Gate-Source voltage	±30	V	
E _{AS}	Single Pulsed Avalanch	2055	mJ	
I _{AR}	Avalanche Current (Note 1)		44	А
E _{AR}	Repetitive Avalanche Energy (Note 1)		30.7	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P _D	Power Dissipation	(T _C = 25°C) - Derate Above 25°C	307 2.45	W W/°C
T _{J,} T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temper	300	°C	

Thermal Characteristics

Symbol	Parameter	FDB44N25	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.41	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient (1 in ² Pad of 2-oz Copepr), Max.	40	°C/W
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambien (Minimum Pad of 2-oz Copper), Max.	62.5	

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Part N	Part Number Top Mark Pa		Package	Packing Method	Reel Size	Тај	be Width	Qu	antity
-		D ² -PAK	PAK Tape and Reel 330 mm		24 mm		800 units		
Electric	al Chara	acteristics T _C = 25	5°C unless o	therwise noted.					
Symbol		Parameter		Conditions		Min.	Тур.	Max	Unit
Off Charac	teristics								<u> </u>
BV _{DSS}	Drain-Sour	ce Breakdown Voltage	V _{GS} =	V _{GS} = 0 V, I _D = 250 μA		250			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient		I _D = 28	$I_D = 250 \ \mu\text{A}$, Referenced to 25°C			0.25		V/∘C
I _{DSS}	Zero Gate Voltage Drain Current			$V_{DS} = 250 V, V_{GS} = 0 V$ $V_{DS} = 200 V, T_{C} = 125^{\circ}C$				1 10	μΑ μΑ
I _{GSSF}	Gate-Body	Leakage Current, Forwa	-	$V_{GS} = 30 V, V_{DS} = 0 V$				100	nA
I _{GSSR}	Gate-Body	Leakage Current, Revers	se V _{GS} =	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$				-100	nA
On Charac	teristics								<u> </u>
V _{GS(th)}	Gate Thres	hold Voltage	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$		3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance		V _{GS} =	10 V, I _D = 22 A			0.058	0.069	Ω
9 _{FS}	Forward Tra	Forward Transconductance		40 V, I _D = 22 A			32		S
Dynamic C	haracteristi	cs							1
C _{iss}	Input Capa	citance	V _{DS} =	$V_{DS} = 25 V, V_{GS} = 0 V,$ f = 1 MHz			2210	2870	pF
C _{oss}	Output Cap	acitance	f = 1 N				450	585	pF
C _{rss}	Reverse Transfer Capacitance						60	90	pF
Switching	Characteris	tics							
t _{d(on)}	Turn-On De	elay Time	V _{DD} = 125 V, I _D = 44 A,				55	120	ns
t _r	Turn-On Ri	se Time	V _{GS} =	V _{GS} = 10 V, R _G = 25 Ω (Note 4)			400	810	ns
t _{d(off)}	Turn-Off De	elay Time					85	180	ns
t _f	Turn-Off Fa	III Time					115	240	ns
Qg	Total Gate	Charge		$V_{DS} = 200 \text{ V}, \text{ I}_{D} = 44 \text{ A},$ $V_{GS} = 10 \text{ V}$			47	61	nC
Q _{gs}	Gate-Source	e Charge	V _{GS} =				18		nC
Q _{gd}	Gate-Drain	Charge		(Note 4)			24		nC
Drain-Sou	rce Diode Cl	naracteristics and Maxi	num Rating	js					
I _S	Maximum (Continuous Drain-Source	Diode Forw	ard Current				44	Α
I _{SM}	Maximum Pulsed Drain-Source Diode F		le Forward (Current				176	Α
V _{SD}	Drain-Sour	ce Diode Forward Voltage	e V _{GS} =	V _{GS} = 0 V, I _S = 44 A				1.4	V
t _{rr}	Reverse Re	ecovery Time		0 V, I _S = 44 A,			195		ns
Q _{rr}	Reverse Re	ecovery Charge	dl _F /dt	=100 A/μs			1.8		μC

Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. L = 1.7 mH, I_{AS} = 44 A, V_DD = 50 V, R_G = 25 $\Omega,$ starting T_J = 25°C.

3. I_{SD} \leq 44 A, di/dt \leq 200 A/µs, V_{DD} \leq BV_{DSS}, starting T_J = 25°C.

4. Essentially independent of operating temperature typical characteristics.

12

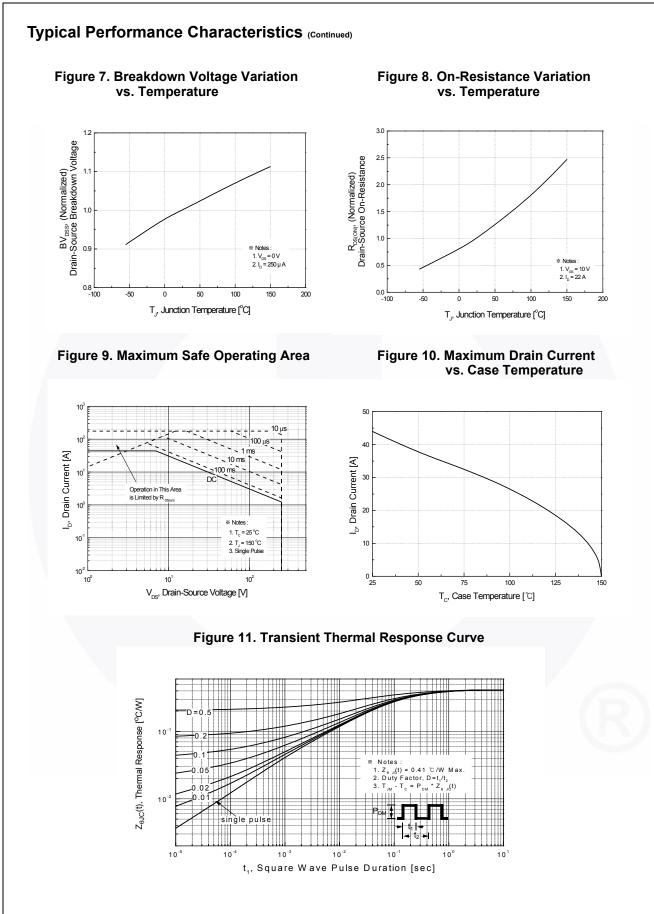
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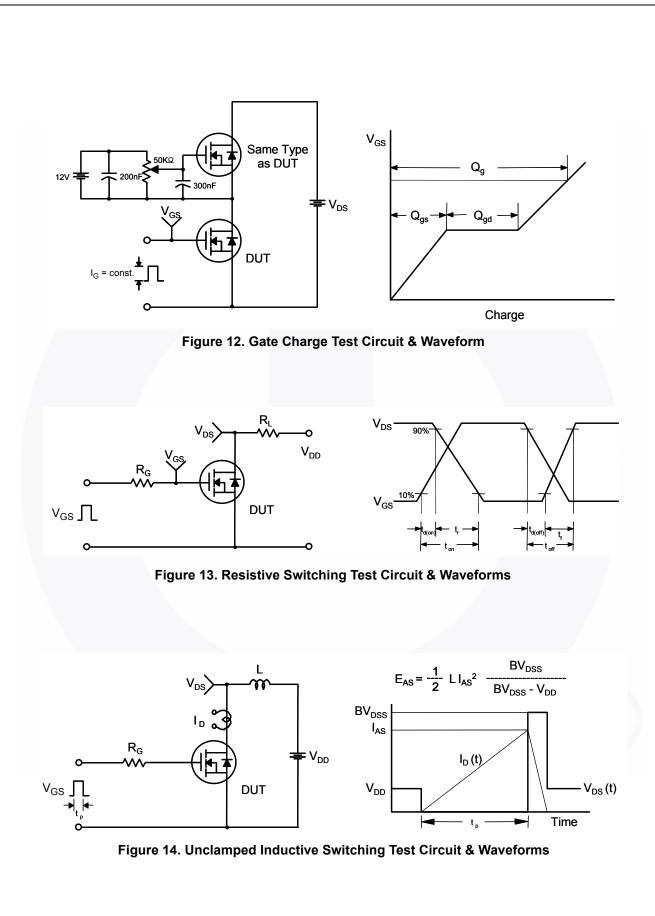
Typical Performance Characteristics Figure 1. On-Region Characteristics **Figure 2. Transfer Characteristics** V_{GS} 15.0 V 10.0 V 8.0 V 7.0 V 6.5 V Тор 10² 10 6.0 V 5.5 V I_b, Drain Current [A] l_p, Drain Current [A] 10 150°C 10 25°C -55°C 100 Notes : 1. V_{DS} = 40V 2. 250µ s Pulse Test 1. 250µ s Pulse Tes 2. T_c = 25°C 10⁰ 10 10 10 10 V_{GS}, Gate-Source Voltage [V] V_{DS}, Drain-Source Voltage [V] Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue 0.125 $R_{\rm Ds(ON)} \left[\Omega ~ \right], \label{eq:rescaled}$ Drain-Source On-Resistance 0.100 Reverse Drain Current [A] 0.07 10V 0.050 150℃ 25 0 V_{GS} = 20V 0.025 € Notes B, = 0\ 1. V_{GS} = 0V 2. 250µ s Pulse Te 10⁰ ∟ 0.2 0.000 100 150 25 50 75 125 0.4 0.6 0.8 1.0 1.2 1.4 1.6 I_D, Drain Current [A] $V_{_{SD}}$, Source-Drain voltage [V] **Figure 6. Gate Charge Characteristics Figure 5. Capacitance Characteristics** 12 6000 + C_{gd} (C_{ds} + C $C_{iss} = C_{gs}$ $C_{ass} = C_{as}$ shorted V_{DS} = 50V Gate-Source Voltage [V] 5000 V_{D5} = 125V V_{DS} = 200V 4000 Capacitances [pF] F 3000 2000 1. V_{GS} = 0 V 2. f = 1 MHz $<_{\rm GS}$ 1000 * Note : L = 44A 0 0 10 20 50 0 30 40 10 10 10 Q_c, Total Gate Charge [nC] V_{DS}, Drain-Source Voltage [V] ©2005 Fairchild Semiconductor Corporation 3

FDB44N25 Rev. C1

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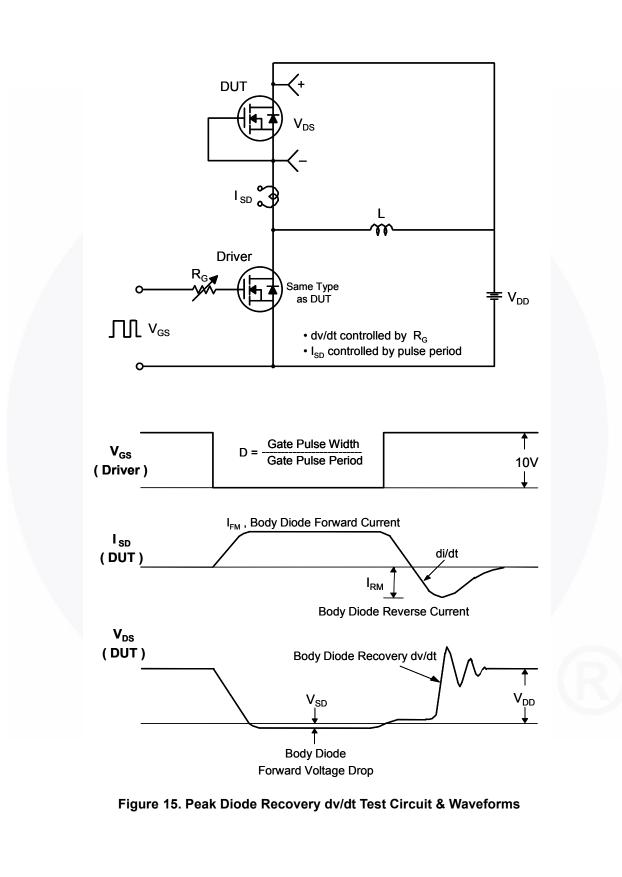
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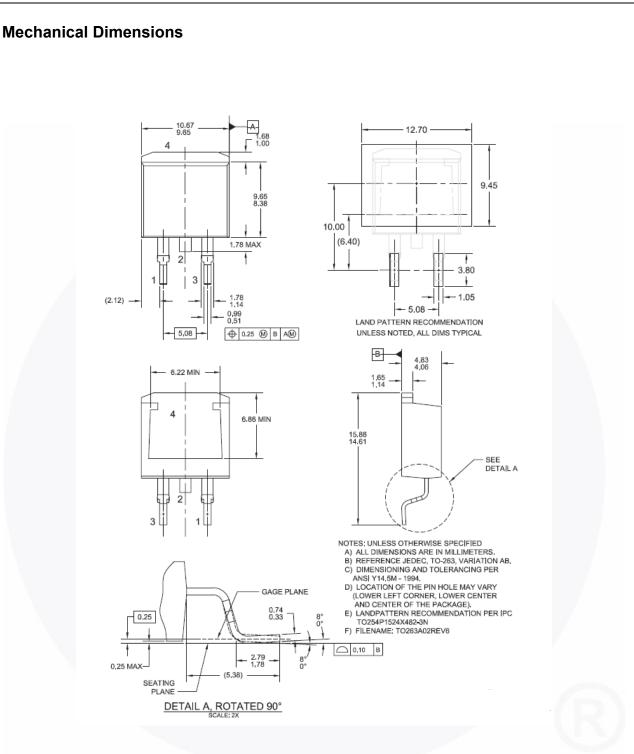


Figure 16. TO263 (D²PAK), Molded, 2-Lead, Surface Mount

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